

विषय विवरण द्वारा दिया गया है। यह विवरण विषय विवरण के अनुसार दिया गया है।

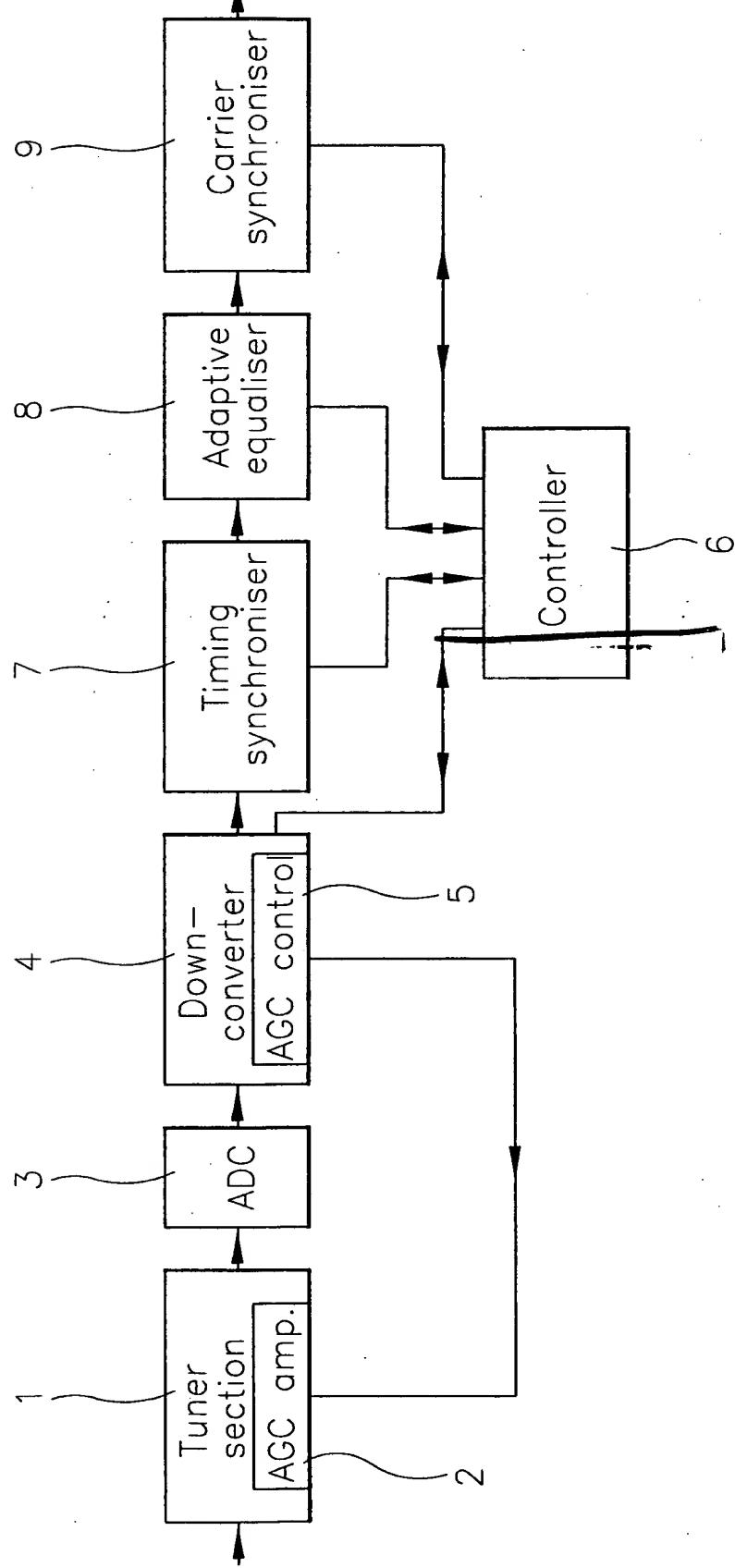


FIG 1

1. Frequency conversion  
2. Down-converter  
3. AGC control  
4. Frequency  
5. Down-converter  
6. Controller  
7. Timing synchroniser (DPLL)  
8. Adaptive equaliser  
9. Carrier synchroniser (DPLL)

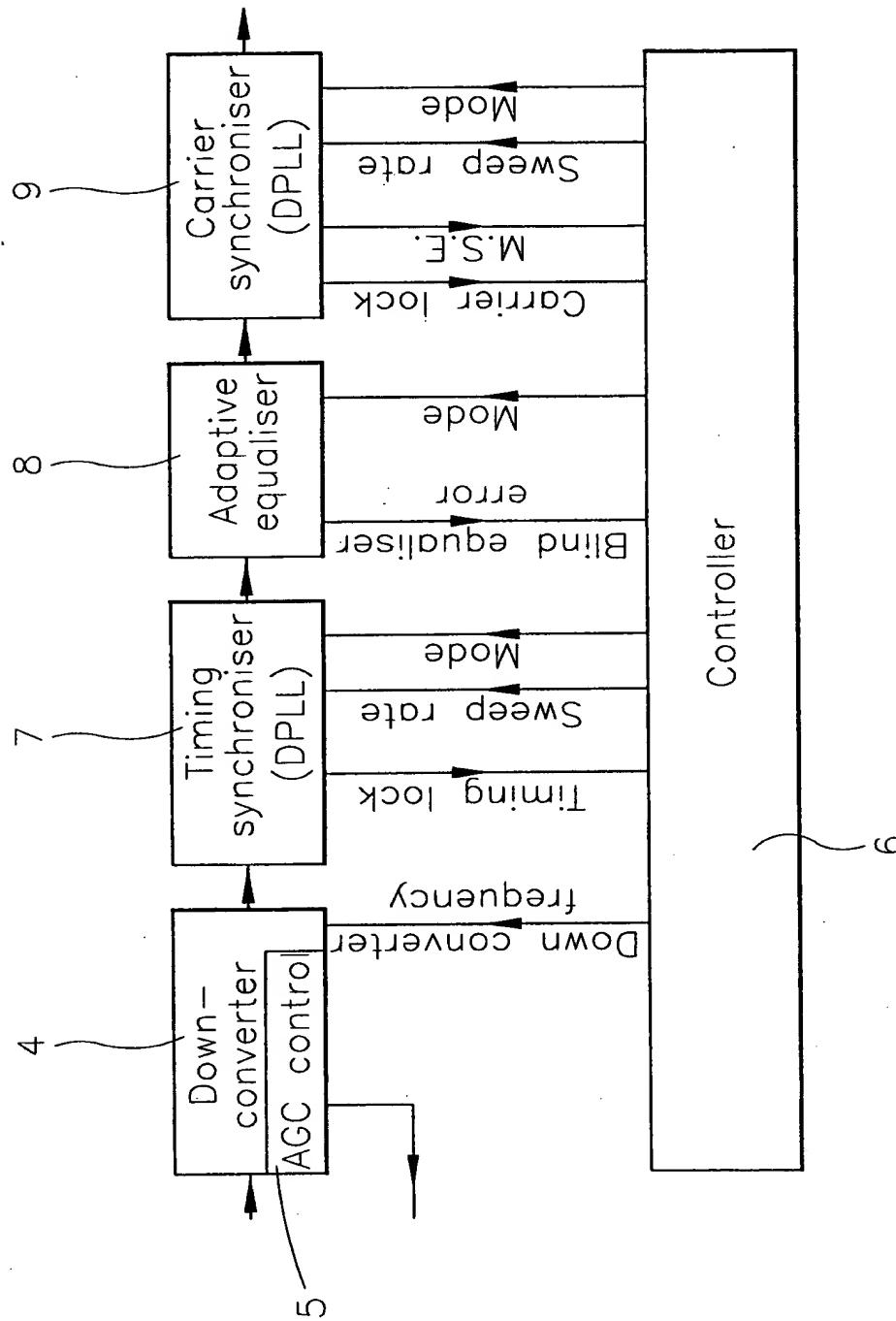
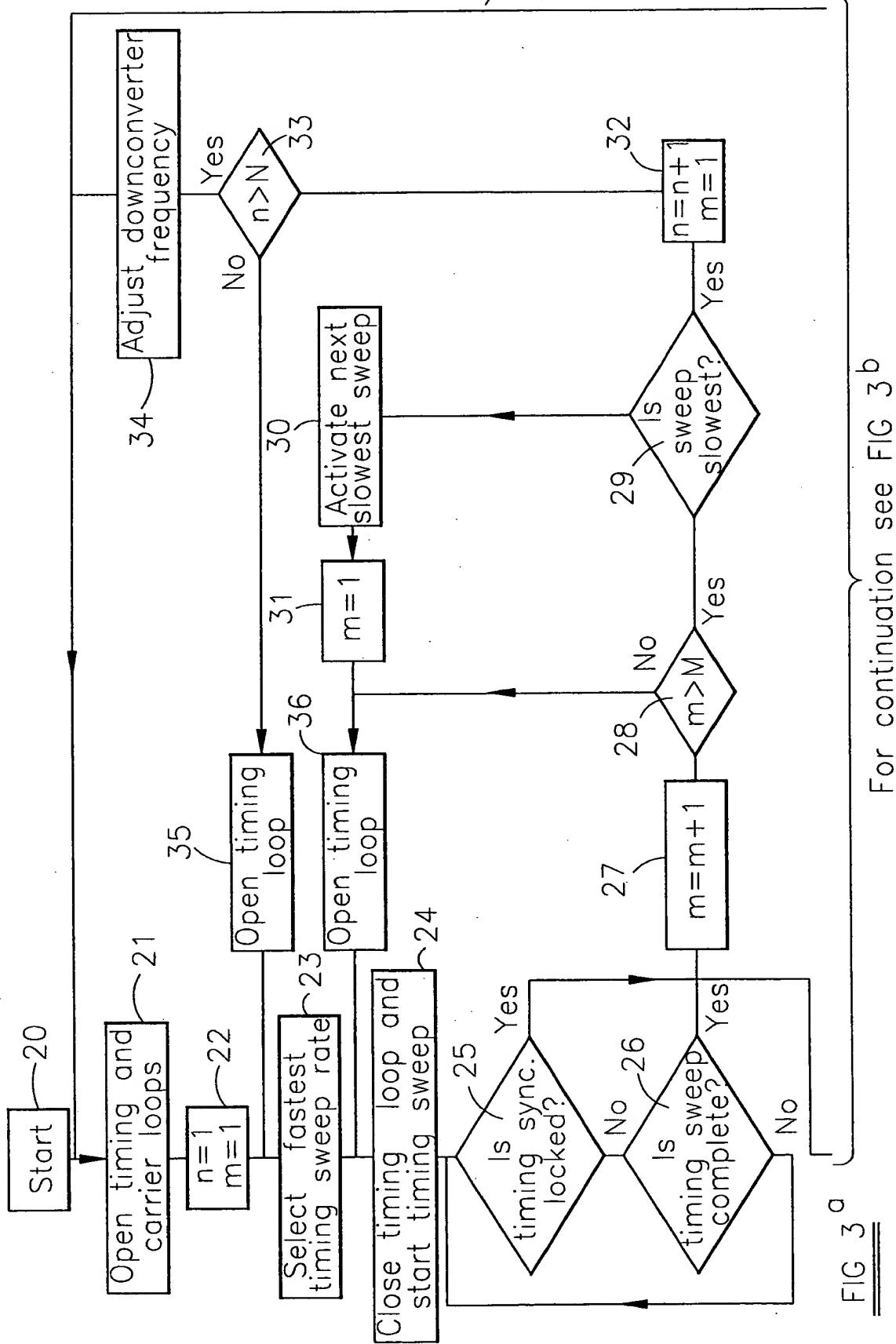


FIG 2



For continuation see FIG 3 a

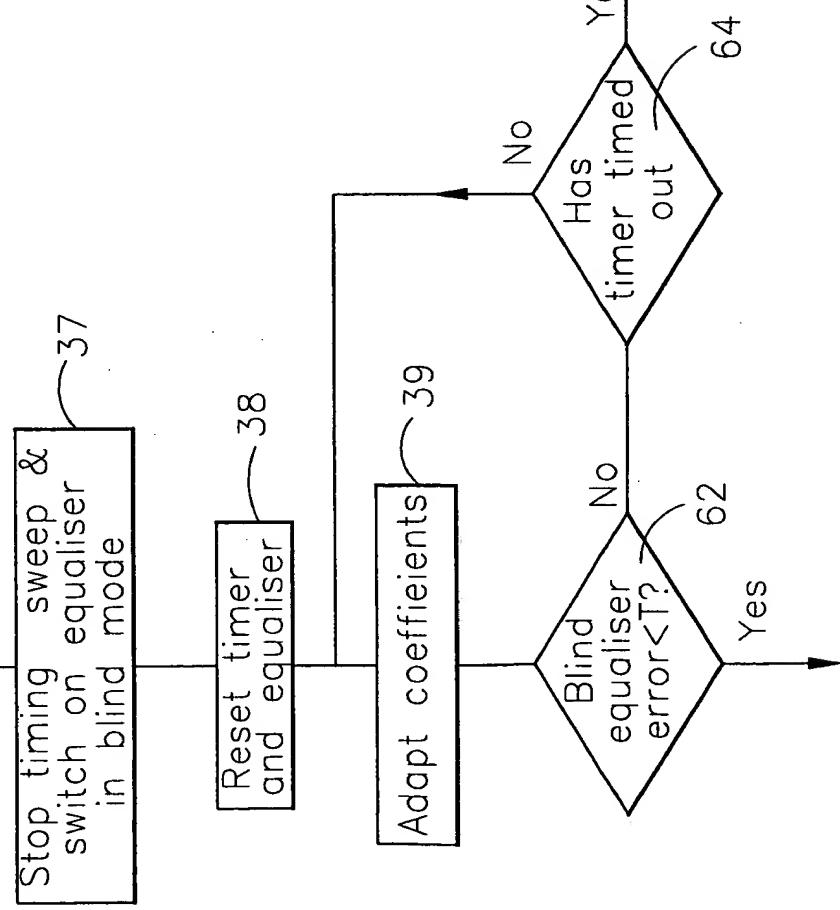
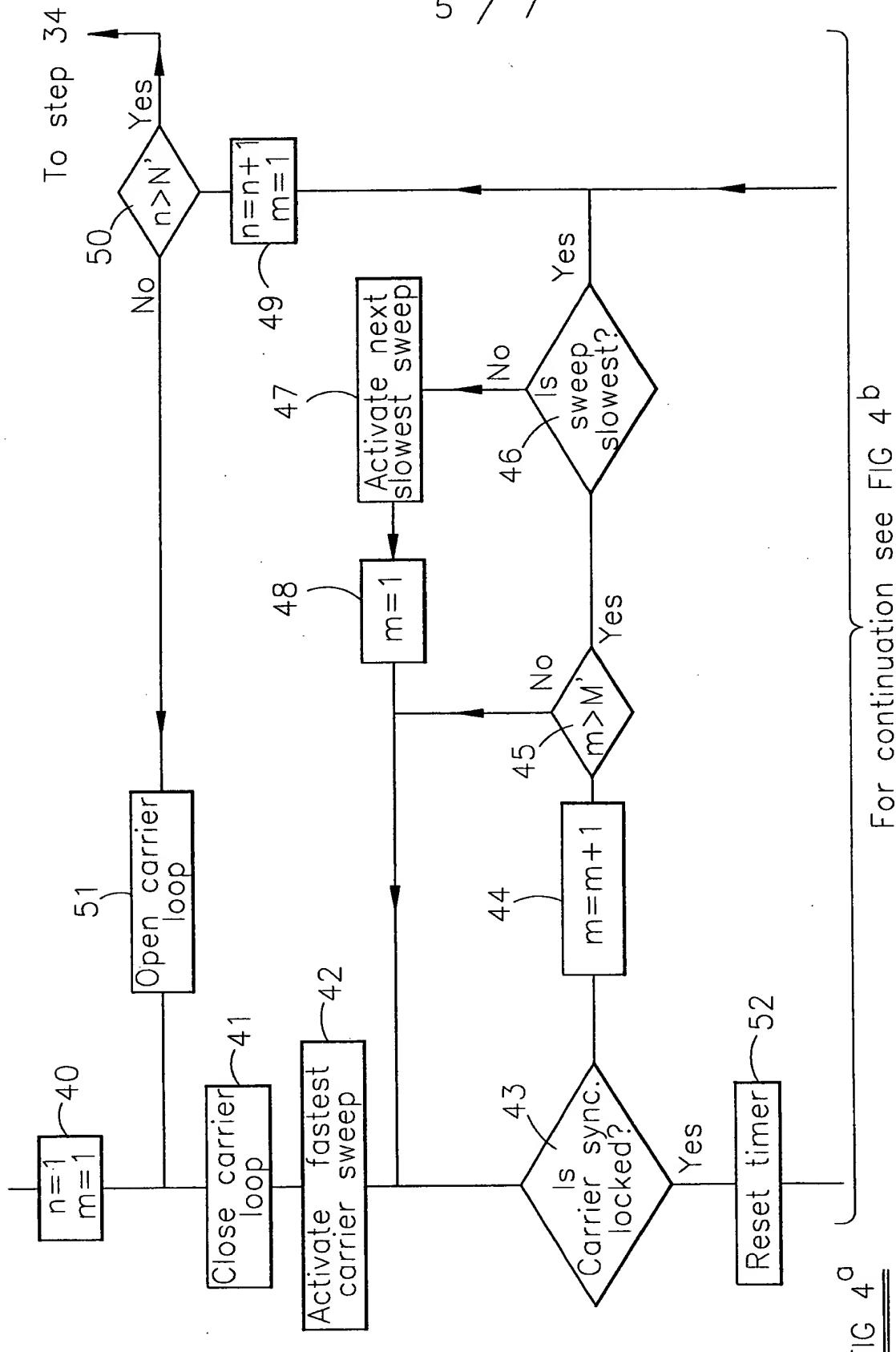
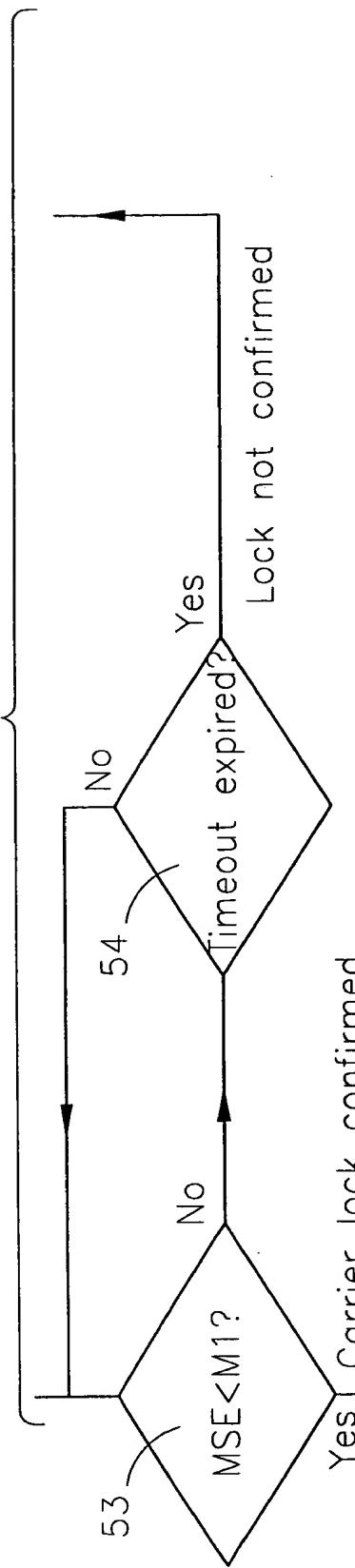


FIG 3 b



For continuation see FIG 4<sup>a</sup>



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To step 21

No

Yes

58

66

Switch equaliser to  
blind mode

Switch equaliser to  
decision feedback mode

For continuation see FIG 4<sup>c</sup>

FIG 4  
b  
=====

For continuation see FIG 4b

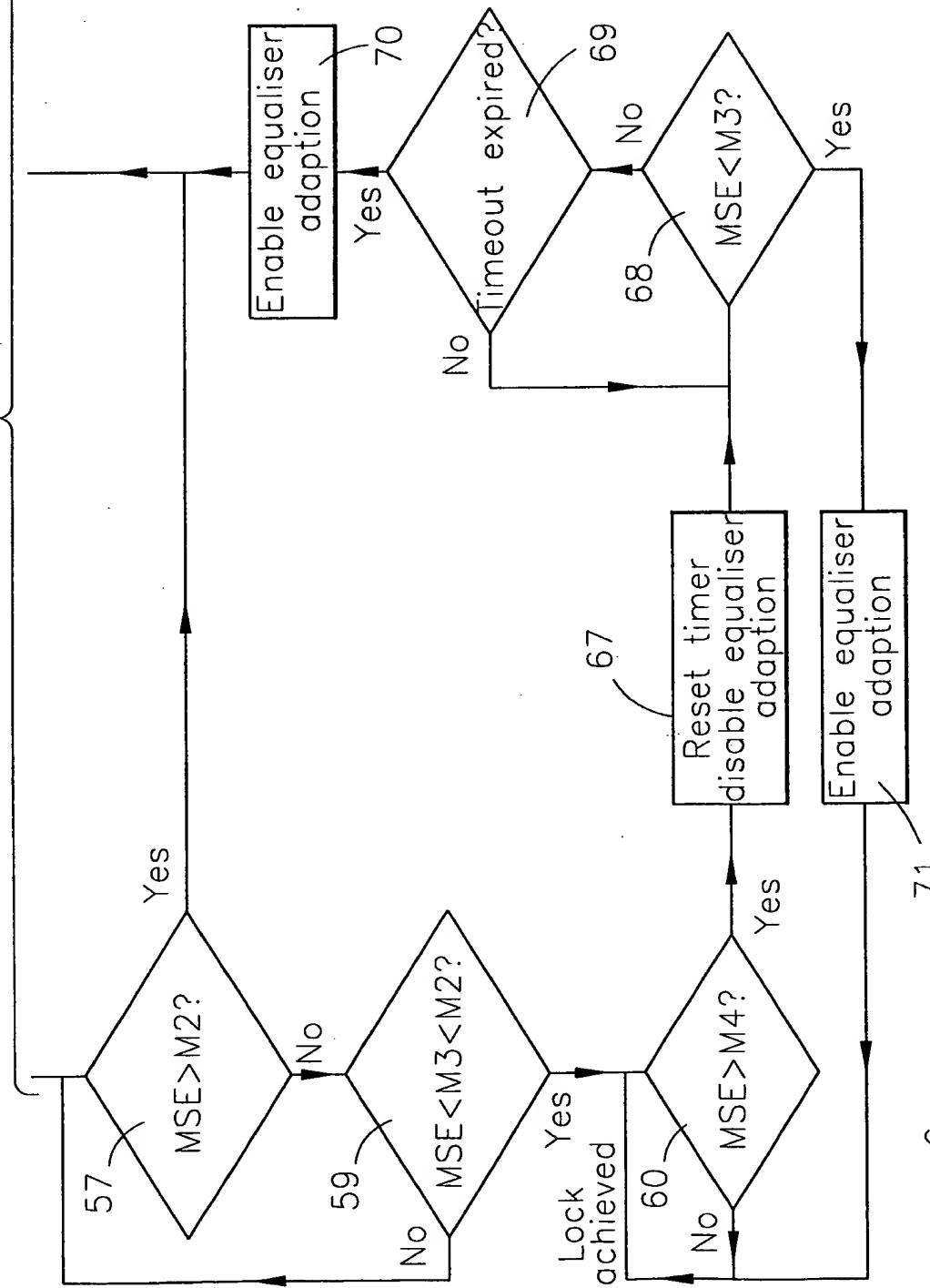


FIG 4  
c